FORM PTO-1449 U.S. Department of Commerce Attorney Docket Number Serial No. Patent and Trademark Office 5646-81 10/643.208 OF DOCUMENTS CITED BY APPLICANT se several sheets if necessary) Applicants: Robert J. Proebsting TEADER . Filing Date: August 18, 2003 Group 3812 2B18 U. S. PATENTS & PATENT APPLICATION PUBLICATIONS Filing Date Examiner Document Initial Number Date Name Class Subclass Appropriate 327 158 6,593,786 7/15/03 Jung 158 327 2 6,586,978 7/1/03 Stief 365 194 3 6,584,021 6/24/03 Heyne et al. 375 371 4 6,539,072 3/25/03 Donnelly et al. 158 5 6,518,807 2/11/03 Cho 327 6 1/7/03 von Kaenel 327 158 6,504,408 12/31/02 Gauthier et al. 327 551 7 6,501,328 327 161 8 6,501,312 12/31/02 Nguyen 9 716 2 2003/0154447 8/14/03 Gauthier et al. 713 400 2003/0154417 8/14/03 Drexler 10 2003/0151433 8/14/03 Takai 327 158 11 158 7/31/03 Reindl 327 12 2003/0141910 327 158 13 2003/0117194 6/26/03 Lee 6/26/03 327 158 14 2003/0117193 Lee 6/12/03 375 376 15 2003/0108139 Jung 5/29/03 375 376 16 2003/0099321 Juan et al. 57 Gomm et al. 331 17 2003/0095009 5/22/03 18 2003/0094984 5/22/03 Weis et al. 327 182 327 12 19 2003/0090296 5/15/03 Yoo 327 157 2003/0071668 4/17/03 Start 20 158 2003/0067335 4/10/03 von Kaenel 327 21 327 158 2003/0058014 3/27/03 Krishnamurty 22 2003/0052719 3/20/03 Na 327 158

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FORM PTO-1449 U.S. Department of Commerce Patent and Trademark Office				Attorney Docket Number 5646-81			Serial No. 10/643,208
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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18 Stylesheet Version v18.0

> Title of Invention

Integrated Circuit Devices Having High Precision Digital **Delay Lines Therein**

Application Number:

10/643208

Confirmation Number:

4050

First Named Applicant:

Robert Proebsting

Attorney Docket Number: 5646-81

Art Unit:

2812 2818

Search string:

(6326826).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
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Signature

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	3	5,614,855	03-25-97	Lee et al.		327	158	
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	5	Lee et al., "A 2.5 of Solid-State Ci					byte/s DRAM	" IEEE Journal
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	12	6,255,969 B1	7-3-01	Crayford		341	100	
	13	6,239,892 B1	5-29-01	Davidson		359	135	
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	16	5,910,740	6-8-99	Underwood		327	149	
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(A)	24	Ryan, Kevin, "DDR SDRAM Functionality and Controller Read Data Capture," Micron Technology, Inc., Volume 8, Issue 3, 1999, pp. 1-24						
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